

Photo DMOS-FET Relay

Description

The **LU934** is a 1-Form B solid state relay in an 6 pin SMD package that employs optically coupled MOSFET technology to provide 3750V/5000V of input to output isolation. The optically coupled input is controlled by a highly efficient GaAlAs infrared LED and MOS FETs on the output side.

Features

- Low driver power requirements (TTL/CMOS Compatible)
- Contact form: Normally-On (1b)
- Load voltage: 60V max.
- On-Resistance: 3Ω max.
- 3750 / 5000 Vrms Input/Output isolation
- Tape & Reel version available

Applications

- Telecommunications (PC, Electronic notepad)
- Measuring and Testing equipment
- Industrial control
- Security equipments
- High speed inspection machine

Outline Dimensions

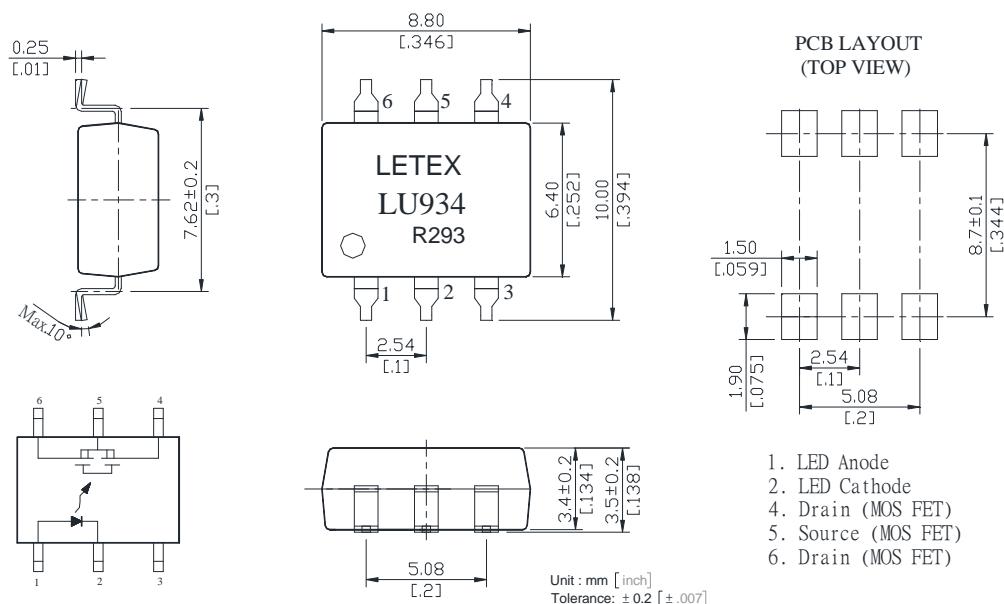


Photo DMOS-FET Relay Specifications

Part Name: LU934

(Load voltage: 60V / Load current: 500mA)

Absolute Maximum Ratings (Ambient Temperature: 25°C)

Item		Symbol	Value	Units	Note
Input	Continuous LED Current	IF	50	mA	
	Peak LED Current	IFP	500	mA	f=100Hz, duty=1%
	LED Reverse Voltage	VR	5	V	
	Input Power Dissipation	PIn	75	mW	
Output	Load Voltage	VL	60	V(AC peak or DC)	
	Load Current	IL	500	mA	
	Peak Load Current	IPeak	0.6	A	1ms(1 pulse)
	Output Power Dissipation	Pout	300	mW	
Total Power Dissipation		PT	350	mW	
I/O Breakdown Voltage		VI/O	3750	Vrms	RH=60%, 1min
I/O Breakdown Voltage(Suffix-V)		VI/O	5000	Vrms	RH=60%, 1min
Operating Temperature		Topr	-40 to +85	°C	
Storage Temperature		Tstg	-40 to +100	°C	
Pin Soldering Temperature		Tsol	260	°C	10 sec max.

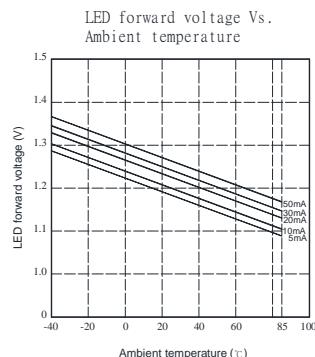
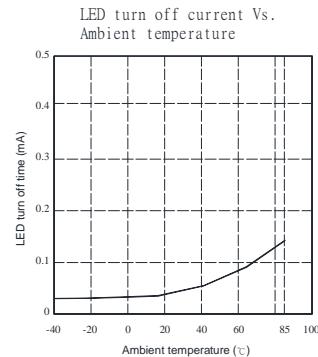
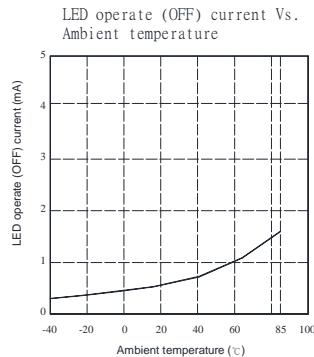
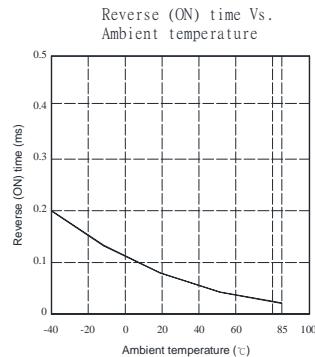
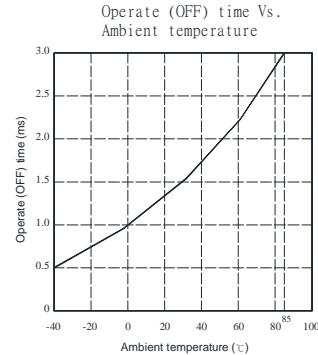
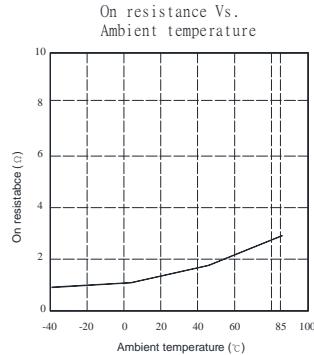
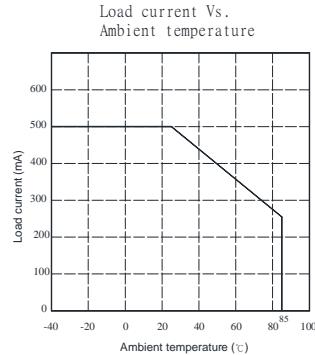
Electrical Specifications (Ambient Temperature: 25°C)

Item		Symbol	MIN.	TYP.	MAX.	Units	Conditions
Input	LED Forward Voltage	VF		1.2	1.5	V	IF=10mA
	Operation LED Current	IFon		0.5	5.0	mA	
	Recovery LED Current	IFoff	0.1	0.4		mA	
	Recovery LED Voltage	VFoff	0.5			V	
Output	On-Resistance	Ron		1	3	Ω	IF=0mA, IL=100mA, Time to flow is within 1 sec.
	Off-State Leakage Current	ILeak			1	uA	IF=10mA, VL=60V
	Output Capacitance	Cout		165		pF	IF=10mA, VL=0V, f=1MHz
Transmission	Turn-Off Time	Toff		0.5	3.0	ms	IF=10mA,
	Turn-On Time	Ton		0.25	1.0	ms	IL=100mA
Coupled	I/O Isolation Resistance	Ri/o	10^{10}			Ω	DC500V
	I/O Capacitance	Ci/o		0.8		pF	f=1MHz

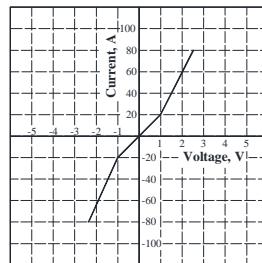


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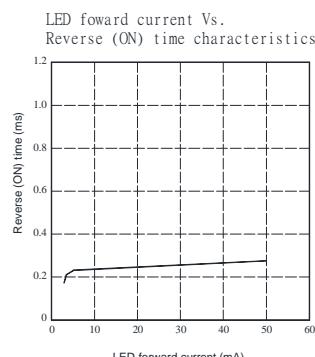
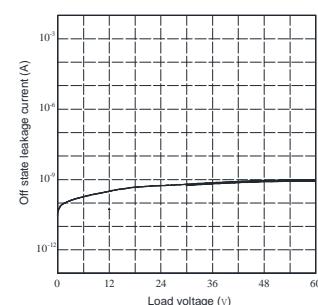
Reference Data



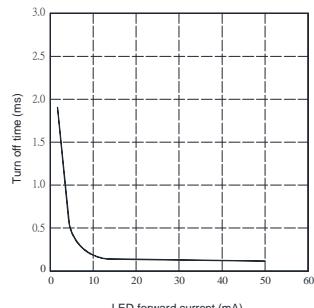
Voltage Vs. current characteristics
of output at MOS portion



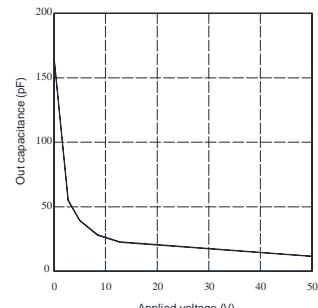
Off state leakage current Vs.
Load voltage characteristics



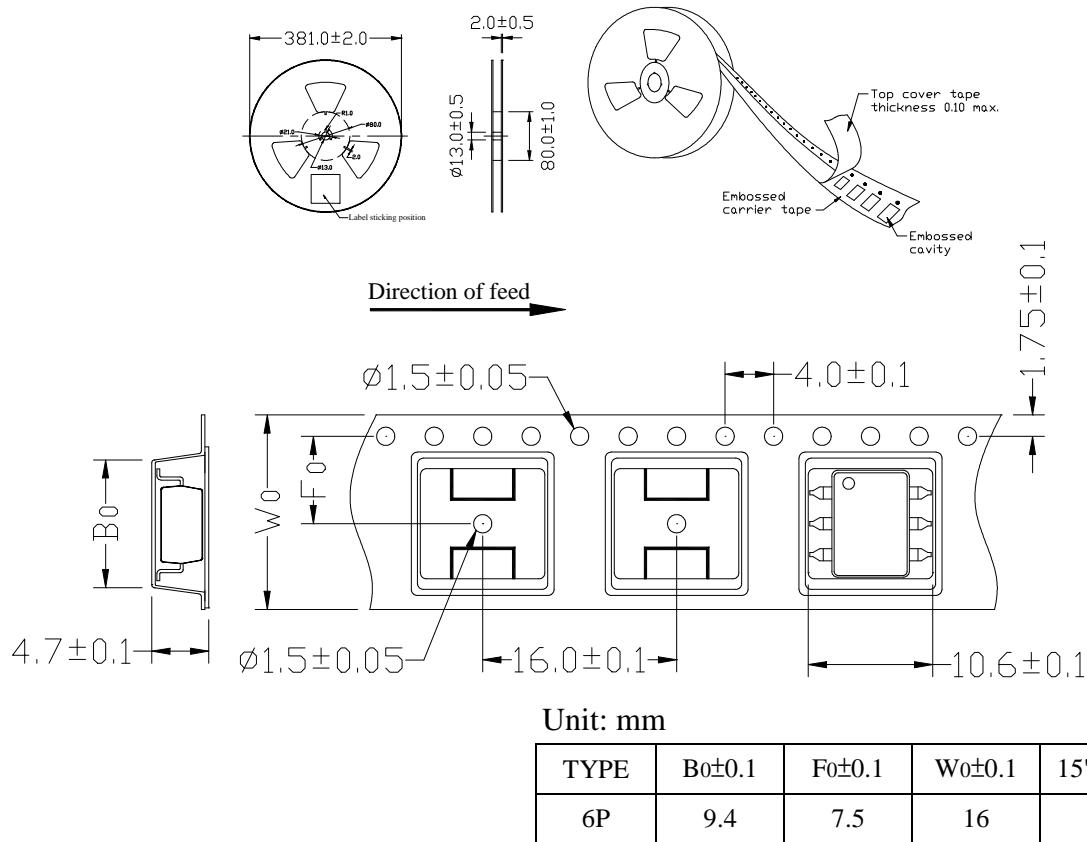
LED foward current Vs.
Operate (OFF) time characteristics



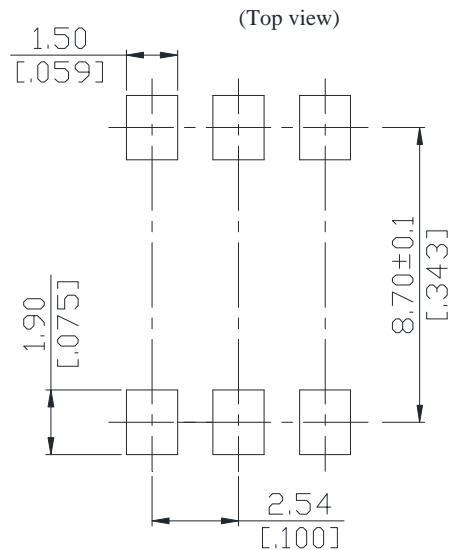
Applied voltage Vs.
output capacitance characteristics



Taping Specifications for Surface Mount Devices

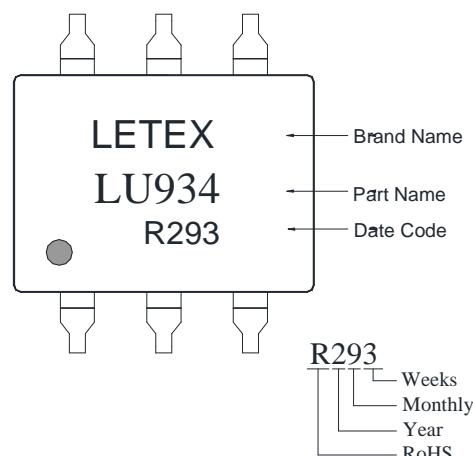


Recommended Mounting Pad



Marking

(Each photo MOS Relay shall be marked with the following information)



- Note:
1. There shall be leader of 230 mm minimum which may consist of carrier and or cover tape follower by a minimum of 160 mm of carrier tape sealed with cover tape.
 2. There shall be a minimum of 160 mm of empty component pockets sealed with cover tape.
 3. Devices are pockets in accordance with EIA standard EIA-481-A and specifications given above.